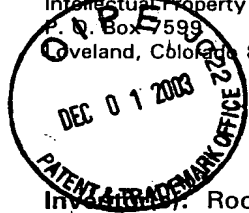


AGILENT TECHNOLOGIES, INC.
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ATTORNEY DOCKET NO. 10030379-1



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Root, et al

Serial No.: 10/712,706

Examiner: Not yet assigned

Filing Date: November 12, 2003

Group Art Unit:

Title: Method for Generating a Circuit Model

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- (a) ☒ Under 37 CFR 1.97(b).
(Within three months of filing national application; or date of entry of national application; or before mailing date of first Office action on the merits; whichever occurs last).
- (b) ☐ Under 37 CFR 1.97(c) together with *either* a:
☐ Statement under CFR 1.97(e), or
☐ \$180.00 fee under 37 CFR 1.17(p).
(After the CFR 1.97(b) time period, but before a final action or notice of allowance, whichever occurs first).
- (c) ☐ Under 37 CFR 1.97(d) together with: a
Statement under 37 CFR 1.97(e), *and*
\$180.00 fee as set forth in 37 CFR 1.17(p).
(After a final action or notice of allowance, whichever occurs first, but before payment of the issue fee).

☐ STATEMENT UNDER 37 CFR 1.97(e)

The undersigned certifies that:

- ☐ Each item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the statement, or
- ☐ No item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned after making reasonable inquiry, was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.

☐ PRIOR APPLICATIONS

References identified with an asterisk (*) in the enclosed PTO Form 1449, were disclosed in prior Patent Application No. , filed , now U.S. Patent No. , and, as such, copies thereof are not included pursuant to the provisions of 37 CFR 1.98(d).

☐ FOREIGN LANGUAGE DOCUMENTS

A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO form 1449, as presently understood by the individual(s) designated in 37 CFR 1.56(c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

12/03/2003 TRESHAM1 00000093 501078 10712706

01 FC:1806 180.00 DA

☒ **FEE AUTHORIZATION**

Please charge to Deposit Account 50-1078 the sum of 180.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 50-1078 pursuant to 37 CFR 1.25.

I hereby certify that this correspondence is being deposited with the United States Postal Service as:

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☐ I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Date of Facsimile:

Typed Name: Calvin Ward

Signature: 

Respectfully submitted,

By 

Calvin B. Ward
Attorney/Agent for Applicant(s)

Reg. No. 30,896

Date: November 25, 2003

Telephone No. (925) 855-0413



FORM PTO-1449 (Modified)

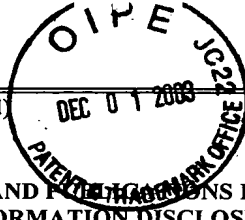
**LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT(S)' INFORMATION DISCLOSURE
STATEMENT**

(Use several sheets if necessary)

ATTNY. DOCKET NO.	SERIAL NO.
10030379-1	10/712,706
APPLICANT Root, et al	
FILING DATE	GROUP ART UNIT
11/12/03	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

A1	Traverso, et al – A Nonlinear Dynamic S/H-ADC Device Model Based on a Modified Volterra Series: Identification Procedure and Commercial CAD Tool Implementation – IEEE Transactions on Instrumentation and Measurement, vol. 52, no. 4, pages 1129-1135, August 2003
A2	Constantini, et al – Accurate Prediction of PHEMT Intermodulation Distortion Using the Nonlinear Discrete Convolution Model – 2002 IEEE MTT-S Digest-pg. 857-860
A3	Mirri, et al – A Modified Volterra Series Approach for the Characterization of Non-Linear Dynamic Systems – IEEE I and M Technology Conference - pages 710-715 - June 4-6, 1996,
A4	Maas - Modeling MESFET's for Intermodulation Analysis of Mixers and Amplifiers – IEEE Transactions on Microwave Theory and Technology , vol. 38, no. 12, pages 1964-1971, Dec. 1990
A5	Leke & Kenney – Behavioral Modeling of Narrowband Microwave Power Amplifiers with Applications in Simulating Spectral Regrowth – 1996 IEEE MTT-S Digest, pg. 1385-1388
A6	Ku, et al – Extraction of Accurate Behavioral Models for Power Amplifiers with Memory Effects using Two-Tone Measurements – 2002 IEEE MTT-S CDROM- pages 139-142
A7	Ku & Kenney – Behavioral Modeling of RF Amplifiers Considering IMD and Spectral Regrowth Asymmetries – 2003 IEEE MTT-S Digest– pages 799-802
A8	J.S. Kenney – Device Level Behavioral Modeling for Microwave Components – 2000 IMS Workshop on Nonlinear CAD – pages 1-39- June 2000
A9	J.S. Kenney – Nonlinear Microwave Design; Extrapolating Beyond S-Parameters- pages 1-27
A10	Ngoya & Larcheveque – Envelop Transient Analysis: A New Method for the Transient and Steady State Analysis of Microwave Communication Circuits and Systems – 1996 IEEE MTT-S Digest pages 1365-1368
A11	Larcheveque, et al – New and Efficient Method for the Multitone Steady-State Circuit Simulation – 1998 IEEE- pages VI 330- VI 333
A12	Soury, et al – A New Behavioral Model taking into account Nonlinear Memory Effects and Transient Behaviors in Wideband SSPAs – 2002 IEEE MTT-S CD-ROM – pages 853 - 856
A13	Ngoya, et al – Accurate RF and Microwave System Level Modeling of Wide Band Nonlinear Circuits, 2000 IEEE-
A14	Soury, et al – Measurement Based Modeling of Power Amplifiers for Reliable Design of Modern Communications Systems – 2003 IEEE MTT-S Digest, pages 795-798
A15	Harkouss, et al – Modeling Microwave Devices and Circuits for Telecommunications System Design – IEEE 1998- pages 128-133



**LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT(S)' INFORMATION DISCLOSURE
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SERIAL NO.

10030379-1

10/712,706

APPLICANT

Root, et al

FILING DATE

GROUP ART UNIT

11/12/03

A16	Ngoya – Frequency Domain Methods for Bottom-UP RF and Microwave Nonlinear Subsystem Modeling – MTT-S 2003 Workshop WSG: Fundamentals of Nonlinear Behavioral Modeling- pages 1-38, June 2003
A17	Borges and Pedro – A Comprehensive Explanation of Distortion Sideband Asymmetries- IEEE Transactions on Microwave Theory and Techniques, vol. 50, no. 9, Sept 2002, pages 2090-2101
A18	Fager, et al – Intermodulation Distortion Behavior in LDMOS Transistor Amplifiers- 2002 IEEE MTT-S CDRM, page 131-134
A19	Pedro, et al – Modeling Nonlinear Behavior of Band-Pass Memoryless and Dynamic Systems – 2003 IEEE MTT-S Digest, pages 2133-2136
A20	Fager, et al – Prediction of IMD in LDMOS Transistor Amplifiers Using a New Large-Signal Model, IEEE Transactions on Microwave Theory and Techniques, vol. 50, no. 12, Dec. 2002, pages 2834-2842
A21	Pedro & Carvalho – Artificial Frequency-Mapping Techniques for Multi-Tone Harmonic Balance – International Microwave symposium 2000 – pages 1-24
A22	Pedro & Carvalho – Mixed Time and Frequency Domain Behavioral Modeling and Simulation – International Microwave Symposium 2003, workshop on Fundamentals of Nonlinear Behavioral Modeling, pages 1-38

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and *not* considered. Include copy of this form with next communication to Applicant(s).